

CANopen Chip CO4012



The frenzel & berg electronic CO4012- E/ A- Chips provide all necessary functions for a CANopen-device. The software- implementation of the CANopen- protocol is already done and tested so an additional programming of the device is not needed. The setup for node- number, baudrate and device configuration is done by external resistors.

system

- CPU 16 Bit
- supply voltage 5V DC
- temperature range -40 .. +105°C
- housing QFP64

in-/ outputs digital / analog

- in-/ outputs 32 max.
- filters for digital inputs configurable
- inputs digital 5V (max.) 32 of 32
- outputs digital 5V (max.) 32 of 32
- inputs analog 0 .. +5V (max.) 8 of 32
- supported modes:
 - mode 0: 16 digital IN / 16 digital OUT
 - mode 1: 32 digital IN
 - mode 2: 32 digital OUT
 - mode 3: 8 analog IN

functions

- output watchdog
- output overload monitoring

CANopen

- CAN- Bus V2.0b
- CAN- baudrates up to 1Mbit
- nodenumber 1 .. 127
- CANopen draft standard DS301 V4.0 / DS401 V2.0
- CANopen diagnosis LEDs (LED outputs)
- PDOs receive/ transmit 1/2
- SDOs server/ client 1/0
- PDO- event timer
- PDO- inhibit timer
- PDO- transmission mode event triggered, synchron, asynchron, cyclic, anti cyclic, remote frame
- variable PDO- identifier
- dynamic PDO- mapping
- emergency
- nodeguarding/ lifeguarding/ heartbeat
- object dictionary / configuration storable
- minimum boot up
- EDS- file

downloads

 [datasheet](#)

 [EDS- file](#)

ordering information

article number	article name
EZ00000.2011.01	CO4012A- FL